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L1	24828705	@ad<"20030916"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L2	983	disabl$3 with internal$2 with (clock CLK)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L3	8	L2 with tracking	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L4	0	L3 and L1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L5	1	L2 same predetermined with tracking	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L6	2	"5239639".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L7	2	"20020054527".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29

L8	2	"5587675".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L9	4	L2 same tracking and L1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:29
L10	17	(disabl$3 "off") with internal$2 with (clock CLK) same tracking and 1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:42
L11	2	"20040064661".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:56
L12	665	712/227.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:56
L13	1734	711/167.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:59
L14	1176	711/105.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:59

L15	722	365/193.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:00
L16	853	365/191.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:00
L17	3508	365/189.01.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:00
L18	1342	713/400.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:06
L19	454	710/58.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:06
L20	297	710/61.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:11
L21	394	712/220.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:12
L22	93	sheffield.in.	US-PGPUB	OR	ON	2007/01/23 14:13

L23	1	22 and "disabling".clm. and "clock signal".clm.	US-PGPUB	OR	ON	2007/01/23 14:14
L24	3	22 and "tracking circuit".clm.	US-PGPUB	OR	ON	2007/01/23 14:21
L25	3	(12 13 14 15 16 17 18 19 20 21) and 1 and (2 10)	US-PGPUB	OR	ON	2007/01/23 14:25
L26	3	(12 13 14 15 16 17 18 19 20 21) and 1 and 2	US-PGPUB	OR	ON	2007/01/23 14:25
L27	3	(12 13 14 15 16 17 18 19 20 21) and 1 and internal$2 with (clock CLK) same tracking	US-PGPUB	OR	ON	2007/01/23 14:26

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1   RaceTrack: efficient detection of data race conditions via adaptive tracking   ▬
Yuan Yu, Tom Rodeheffer, Wei Chen
October 2005 **ACM SIGOPS Operating Systems Review , Proceedings of the twentieth ACM symposium on Operating systems principles SOSP '05**, Volume 39 Issue 5
Publisher: ACM Press

Full text available: 📄 pdf(321.34 KB)   Additional Information: full citation, abstract, references, citings, index terms

Bugs due to data races in multithreaded programs often exhibit non-deterministic symptoms and are notoriously difficult to find. This paper describes RaceTrack, a dynamic race detection tool that tracks the actions of a program and reports a warning whenever a suspicious pattern of activity has been observed. RaceTrack uses a novel hybrid detection algorithm and employs an adaptive approach that automatically directs more effort to areas that are more suspicious, thus providing more accurate war ...

Keywords: race detection, virtual machine instrumentation

2   AAMP: a multiprocessor approach for operating system and application migration   ▬
Bob Beck
April 1990 **ACM SIGOPS Operating Systems Review**, Volume 24 Issue 2
Publisher: ACM Press
Full text available: 📄 pdf(1.16 MB)   Additional Information: full citation

3   Illustrative risks to the public in the use of computer systems and related technology   ▬
Peter G. Neumann
January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1
Publisher: ACM Press
Full text available: 📄 pdf(2.54 MB)   Additional Information: full citation

4   Soft timers: efficient microsecond software timer support for network processing   ▬
Mohit Aron, Peter Druschel
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the**

seventeenth ACM symposium on Operating systems principles SOSP '99, Volume 33 Issue 5
Publisher: ACM Press

Full text available: pdf(1.65 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

This paper proposes and evaluates soft timers, a new operating system facility that allows the efficient scheduling of software events at a granularity down to tens of microseconds. Soft timers can be used to avoid interrupts and reduce context switches associated with network processing without sacrificing low communication delays.More specifically, soft timers enable transport protocols like TCP to efficiently perform rate-based clocking of packet transmissions. Experiments show that rate-base ...

5   HIP: hybrid interrupt-polling for the network interface
Constantinos Dovrolis, Brad Thayer, Parameswaran Ramanathan
October 2001 **ACM SIGOPS Operating Systems Review**, Volume 35 Issue 4
Publisher: ACM Press

Full text available: pdf(989.34 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The standard way to notify the processor of a network event, such as the arrival or transmission of a packet, is through interrupts. Interrupts are more effective than polling, in terms of the per packet send/receive latency. Interrupts, however, incur a high overhead both during and after the interrupt handling, because modern superscalar processors use long pipelines, out-of-order and speculative execution, and multi-level memory systems, all of which tend to increase the interrupt overhead in ...

6   Thoth, a portable real-time operating system
David R. Cheriton, Michael A. Malcolm, Lawrence S. Melen, Gary R. Sager
February 1979 **Communications of the ACM**, Volume 22 Issue 2
Publisher: ACM Press

Full text available: pdf(1.23 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

Thoth is a real-time operating system which is designed to be portable over a large set of machines. It is currently running on two minicomputers with quite different architectures. Both the system and application programs which use it are written in a high-level language. Because the system is implemented by the same software on different hardware, it has the same interface to user programs. Hence, application programs which use Thoth are highly portable. Thoth encourages structuring progr ...

Keywords: minicomputer, operating systems, portability, real time

7   Status report of the graphic standards planning committee of ACM/SIGGRAPH: State-of-the-art of graphic software packages
Compuater Graphics staff
September 1977 **ACM SIGGRAPH Computer Graphics**, Volume 11 Issue 3
Publisher: ACM Press

Full text available: pdf(9.03 MB)   Additional Information: <u>full citation</u>, <u>references</u>

8   An open-source CVE for programming education: a case study: An open-source CVE for programming education: a case study
Andrew M. Phelps, Christopher A. Egert, Kevin J. Bierre, David M. Parks
July 2005 **ACM SIGGRAPH 2005 Courses SIGGRAPH '05**

Publisher: ACM Press

Full text available: 🔁 pdf(7.92 MB)   Additional Information: <u>full citation</u>, <u>references</u>

9 <u>Status report of the graphic standards planning committee</u>

Computer Graphics staff

August 1979 **ACM SIGGRAPH Computer Graphics**, Volume 13 Issue 3

Publisher: ACM Press

Full text available: 🔁 pdf(15.01 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>

10 <u>Exploiting perception in high-fidelity virtual environments: Exploiting perception in</u> <u>high-fidelity virtual environments</u>
<u>Additional presentations from the 24th course are available on the citation</u> <u>page</u>

Mashhuda Glencross, Alan G. Chalmers, Ming C. Lin, Miguel A. Otaduy, Diego Gutierrez

July 2006 **ACM SIGGRAPH 2006 Courses SIGGRAPH '06**

Publisher: ACM Press

Full text available: 🔁 <u>pdf(5.07 MB)</u> 🎥 Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>
<u>mov(68:6 MIN)</u>

The objective of this course is to provide an introduction to the issues that must be considered when building high-fidelity 3D engaging shared virtual environments. The principles of human perception guide important development of algorithms and techniques in collaboration, graphical, auditory, and haptic rendering. We aim to show how human perception is exploited to achieve realism in high fidelity environments within the constraints of available finite computational resources.In this course w ...

Keywords: collaborative environments, haptics, high-fidelity rendering, human-computer interaction, multi-user, networked applications, perception, virtual reality

11 <u>Illustrative risks to the public in the use of computer systems and related technology</u>

Peter G. Neumann

January 1994 **ACM SIGSOFT Software Engineering Notes**, Volume 19 Issue 1

Publisher: ACM Press

Full text available: 🔁 pdf(2.24 MB)   Additional Information: <u>full citation</u>, <u>citings</u>, <u>index terms</u>

12 <u>Hybrid dynamic data race detection</u>

Robert O'Callahan, Jong-Deok Choi

June 2003 **ACM SIGPLAN Notices , Proceedings of the ninth ACM SIGPLAN**
symposium on Principles and practice of parallel programming PPoPP '03,
Volume 38 Issue 10

Publisher: ACM Press

Full text available: 🔁 <u>pdf(158.47 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u>
<u>terms</u>

We present a new method for dynamically detecting potential data races in multithreaded programs. Our method improves on the state of the art in accuracy, in usability, and in overhead. We improve accuracy by combining two previously known race detection techniques -- *lockset-based detection and happens-before-based detection* -- to obtain fewer false positives than lockset-based detection alone. We enhance usability by reporting more information about detected races than any previous dyna ...

Keywords: Java, dynamic race detection, happens-before, lockset hybrid

13 <u>The embedded machine: predictable, portable real-time code</u>

Thomas A. Henzinger, Christoph M. Kirsch

May 2002 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2002 Conference on Programming language design and implementation PLDI '02**, Volume 37 Issue 5

Publisher: ACM Press

Full text available: <u>pdf(223.85 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The Embedded Machine is a virtual machine that mediates in real time the interaction between software processes and physical processes. It separates the compilation of embedded programs into two phases. The first, platform-independent compiler phase generates E code (code executed by the Embedded Machine), which supervises the timing ---not the scheduling--- of application tasks relative to external events, such as clock ticks and sensor interrupts. E~code is portable and exhibits, given an inpu ...

Keywords: real time, virtual machine

14 <u>Networked sensor energy management: A modular power-aware microsensor with >1000X dynamic power range</u>

Brian Schott, Michael Bajura, Joe Czarnaski, Jaroslav Flidr, Tam Tho, Li Wang

April 2005 **Proceedings of the 4th international symposium on Information processing in sensor networks IPSN '05**

Publisher: IEEE Press

Full text available: <u>pdf(621.54 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

We introduce a power-aware microsensor architecture supporting a wide operational power range (from <1mW to >10W). The platform consists of a family of modules that follow a common set of design principles. Each module includes a local power microcontroller, power switches, and isolation switches to enable independent power-down control of modules and module subsystems. Processing resources are scaled appropriately on each module for their role in the collective system. Hard real-time func ...

Keywords: component, low power embedded systems, microsensors, power aware computing

15 <u>The application accelerator illustration system</u>

Michael S. Miller, Howard Cunningham, Chan Lee, Steven R. Vegdahl

June 1986 **ACM SIGPLAN Notices , Conference proceedings on Object-oriented programming systems, languages and applications OOPLSA '86**, Volume 21 Issue 11

Publisher: ACM Press

Full text available: <u>pdf(629.30 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The Application Accelerator Illustration System is a prototype of an integrated CAD environment that supports the development of application-specific integrated circuits. The current implementation features a hardware description language compiler, timing analyzer, functional simulator, waveform tracer, and data path place and route facility. The system is implemented in Smalltalk-80™.

16 Architecture of the IBM system/370

Richard P. Case, Andris Padegs

January 1978 **Communications of the ACM**, Volume 21 Issue 1

Publisher: ACM Press

Full text available: pdf(2.78 MB)   Additional Information: full citation, abstract, references, citings, index terms

This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

17 The information furnace: consolidated home control

Diomidis D. Spinellis

May 2003 **Personal and Ubiquitous Computing**, Volume 7 Issue 1

Publisher: Springer-Verlag

Full text available: pdf(488.36 KB)   Additional Information: full citation, abstract, citings, index terms

The Information Furnace is a basement-installed PC-type device that integrates existing consumer home-control, infotainment, security and communication technologies to transparently provide accessible and value-added services. A modern home contains a large number of sophisticated devices and technologies. Access to these devices is currently provided through a wide variety of disparate interfaces. As a result, end users face a bewildering array of confusing user-interfaces, access modes a ...

Keywords: Automation, Consumer electronics, Home-control, Multi-modal interfaces

18 Research tools: MiNT-m: an autonomous mobile wireless experimentation platform

Pradipta De, Ashish Raniwala, Rupa Krishnan, Krishna Tatavarthi, Jatan Modi, Nadeem Ahmed Syed, Srikant Sharma, Tzi-cker Chiueh

June 2006 **Proceedings of the 4th international conference on Mobile systems, applications and services MobiSys 2006**

Publisher: ACM Press

Full text available: pdf(803.48 KB)   Additional Information: full citation, abstract, references, index terms

Limited fidelity of software-based wireless network simulations has prompted many researchers to build testbeds for developing and evaluating their wireless protocols and mobile applications. Since most testbeds are tailored to the needs of specific research projects, they cannot be easily reused for other research projects that may have different requirements on physical topology, radio channel characteristics or mobility pattern. In this paper, we describe the design, implementation and evalua ...

Keywords: autonomous operation, mobility, topology reconfiguration, wireless experimentation platform, wireless testbed

19 A comparative study of environments for database system implementation

M. Teresa Suarez Fernandez, H. Rex Hartson

August 1978 **ACM SIGMINI Newsletter , Proceedings of the first SIGMINI symposium on Small systems SIGMINI '78**, Volume 4 Issue 4

Publisher: ACM Press

Full text available: 🗎 pdf(742.67 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> Two specific environments for implementation of experimental DataBase Management Systems (DBMS) are compared, especially in light of performance measurement: IBM 370 and HP-2100A. The HP-2100A was selected for a skeletal implementation to study performance parameters further. The DBMS structure, some performance results, and several difficulties encountered are described. The HP-2100A minicomputer did provide "hands-on" experience and more direct control to customize disk access ...

20 <u>Collision detection and proximity queries</u>

Sunil Hadap, Dave Eberle, Pascal Volino, Ming C. Lin, Stephane Redon, Christer Ericson August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: 🗎 pdf(11.22 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>

> This course will primarily cover widely accepted and proved methodologies in collision detection. In addition more advanced or recent topics such as continuous collision detection, ADFs, and using graphics hardware will be introduced. When appropriate the methods discussed will be tied to familiar applications such as rigid body and cloth simulation, and will be compared. The course is a good overview for those developing applications in physically based modeling, VR, haptics, and robotics.

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